

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: J. Durkot
Serial No.: 10/616,932
Filed: 07/11/03
Title: Method of Testing Printed Circuit Board
Opening Spaces

Group Art Unit: 2858
Examiner:
Endicott Interconnect Technologies
1701 North Street
Endicott, NY 13760

Commissioner For Patents
P.O. Box 1450
Arlington, VA 223130-1450

Sir:

STATUS INQUIRY

Applicants request the status of the above application. The application was filed on 07/11/03. A copy of the return postcard is attached for the Examiner's convenience. A Filing Receipt was received 10/14/03. A copy is also attached for the Examiner's convenience. A Notice to File Corrected Application Papers was received 10/09/03. A response to this Notice was filed 10/20/03, a copy of the return post card attached herewith, once again for the Examiner's convenience.

Because Applicants have heard nothing further as of this date, a status is requested. Please advise when we may expect a communication with respect to the above-referenced application. A stamped, self-addressed envelope is enclosed herewith for your use in responding to this request.

An e-mail response to the undersigned at the email address below or a phone response is encouraged to expedite a response to this inquiry.

Telephone: (561) 575-3608
Fax : (561) 745-2741

Respectfully submitted,

Lawrence R. Fraley
Lawrence R. Fraley

Reg. No: 26,885

Attorney for Applicants

Date: 12/26/04

e-mail: Lawrence.Fraley@eitny.com



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JUL 29 2003

Date: 7/10/03
~~Express Mail No.~~ Airborne Airbill #5994944561

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- | | |
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| <input checked="" type="checkbox"/> Application | <input type="checkbox"/> Issue Fee |
| <input checked="" type="checkbox"/> Declaration <u>2</u> pgs. | <input type="checkbox"/> Draftsman w/Dwgs. |
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| <input checked="" type="checkbox"/> IDS/PTO-1449/Refs. | |
| <input type="checkbox"/> Amendment | |

Title Method Of Testing Printed Circuit Board
Opening Spacings

15 Pgs. Spec. 11 Claims 4 Shts. Of Drawing
\$750.00 Filing Fee Attorney Lawrence R. Fraley
Docket EI-2-03-007
In Appln. Of J. Durkot
Serial/Inter. Number
Filed Herewith

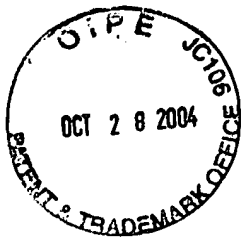
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07/11/03

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Certificate of Mailing under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with United States Postal Service in an envelope addressed to:

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On 10/26/04.

Signature Christine Lang

Christine Lang
Typed or printed name of person of signing Certificate

EI-2-03-007 – Status Inquiry, copy of return postcard, copy of filing receipt, copy of Notice to File Corrected Application Papers, copy of return postcard (response to Notice to File Corrected Application Papers), postcard.

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/616,932	07/11/2003	2858	750	EI-2-03-007	4	11	1

CONFIRMATION NO. 3023

Lawrence R. Fraley, IP Law Counsel
Endicott Interconnect Technologies, Inc.
1701 North Street, FBU/257-2 AA12
Endicott, NY 13760

FILING RECEIPT



OC000000011019776

Date Mailed: 10/09/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

John Durkot, Binghamton, NY;

Assignment For Published Patent Application

Endicott Interconnect Technologies, Inc., Endicott, NY;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 10/09/2003

Projected Publication Date: To Be Determined - pending completion of Corrected Papers

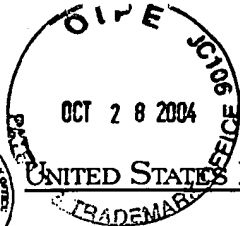
Non-Publication Request: No

Early Publication Request: No

Title

Method of testing printed circuit board opening spacings

Preliminary Class



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
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APPLICATION NUMBER	FILING OR 371 (e) DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
10/616,932	07/11/2003	John Durkot	EI-2-03-007

CONFIRMATION NO. 3023

FORMALITIES LETTER



OC000000011019777

Lawrence R. Fraley, IP Law Counsel
 Endicott Interconnect Technologies, Inc.
 1701 North Street, FBU/257-2 AA12
 Endicott, NY 13760

Date Mailed: 10/09/2003

NOTICE TO FILE CORRECTED APPLICATION PAPERS

Filing Date Granted

An application number and filing date have been accorded to this application. The application is informal since it does not comply with the regulations for the reason(s) indicated below. Applicant is given TWO MONTHS from the date of this Notice within which to correct the informalities indicated below. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

The required item(s) identified below must be timely submitted to avoid abandonment:

- Replacement drawings in compliance with 37 CFR 1.84 and 37 CFR 1.121 are required. The drawings submitted are not acceptable because:
 - The drawings must be made on paper that has a white background (see 37 CFR 1.84 (e)). For example, drawings on graph paper, lined paper, or paper that has a non-white background are not acceptable. See Figure(s) 1-8.

Replies should be mailed to: Mail Stop Missing Parts
 Commissioner for Patents
 P.O. Box 1450
 Alexandria VA 22313-1450

*A copy of this notice **MUST** be returned with the reply.*

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| _____ signed _____ unsigned | <input checked="" type="checkbox"/> Cert. Of Mailing |
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| <input type="checkbox"/> IDS/PTO-1449/Refs. | <input checked="" type="checkbox"/> Correct App. Papers |
| <input type="checkbox"/> Amendment | <input checked="" type="checkbox"/> Response to Notice |

Title Method Of Testing Printed Circuit Board

Opening Spacings

_____ Pgs. Spec. _____ Claims _____ Shts. Of Drawing

_____ Filing Fee Attorney Lawrence Fraley

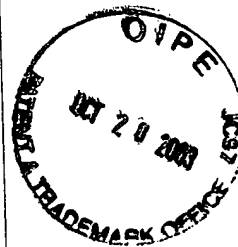
Docket EI-2-03-007

In Appln. Of J. Durkot

Serial/Inter. Number 10/616,932

Filed 7/11/03

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